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DOCKET NUMBER: AUS920030974US1

DATE: January 16, 2004

Sir:

Transmitted herewith for filing is the Patent Application of:

Inventors: Rajat Chaudhry, Brian King Flachs, Daniel Lawrence Stasiak

For: Method and Apparatus for VLSI Clock Gated Power Estimation Using LCB Counts

Assignee Name: International Business Machines Corporation

Assignee Residence: Armonk, New York

Enclosed are:

X __17 Patent Specification including an Abstract

X 6 Pages of Claims

X 5 Sheet(s) of drawings. Informal X Formal

X Declaration(s) and Power of Attorney

X Form PTO 1595 and Assignment of the Invention to IBM Corporation

A certified copy of a ____ application.

Information Disclosure Statement, PTO 1449 and copies of _____ references.

The filing fee has been calculated as shown below:

FOR	Number Filed		Number Extra		Rate		Basic Fee \$770.00
Total Claims	24	-20 =	4	X	\$ 18	=	\$ 72.00
Independent Claims	,3	- 3 =	Ø	Х	\$ 86	=	\$ 0.00
Multiple Dependent Claims	00			X	\$290	=	\$
				Total	Filing Fee	=	\$\$4200

- X Please charge my Deposit Account No. <u>09-0447</u> in the amount of \$34300. A duplicate copy of this sheet is enclosed.
- X The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account <u>09-0447</u>. A duplicate copy of this sheet is enclosed.
 - X Any additional filing fees required under 37 CFR 1.16.
 - \underline{X} Any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

Robert M Carwell

Registration No. 28,499

Intellectual Property Law Dept.

IBM Corporation

11400 Burnet Road

Austin, Texas 78758

Telephone (512) 823-1017

031431 U.S. PTO 10/759936 011604